

Substitute for Form 1449/PTO

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

## Complete if Known

Application Number	10/607,769
Filing Date	June 27, 2003
First Named Inventor:	Scott A. Hareland
Art Unit	Unassigned
Examiner Name	Unassigned
Attorney Docket Number	42P15685

Sheet 1 of

## U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)				
MWS		US-	5,578,513	11/26/1996	Maegawa	
MWS		US-	6,483,156 B1	11/19/2002	Adkisson et al.	
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## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				

Examiner  
Signature

/Matthew Such/

Date Considered

10/15/2006

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MWS		US-	6,716,684 B1	4/6/2004	Krivokapic et al.	
MWS		US-	6,680,240 B1	1/20/2004	Maszara	
MWS		US-	6,562,665 B1	5/13/2003	Yu	
MWS		US-	6,525,403 B2	2/25/2003	Inaba et al.	
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MWS		US-	5,578,513	11/26/1996	Maegawa	
MWS		US-	6,413,802 B1	7/2/2002	Hu et al.	
MWS		US-	6,730,964 B2	5/4/2004	Horiuchi	
		US-				
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MWS		EP	0 623 963 A1	11/9/1994	Siemens AG		
MWS		WO	02/43151A	5/30/2002	Hitachi ULSI Sys Co Ltd		

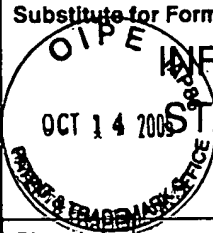
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				Filing Date	June 27, 2002
				First Named Inventor:	Scott A. Hareland et al.
				Art Unit	Unknown
				Examiner Name	Unknown
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<b>NON PATENT LITERATURE DOCUMENTS</b>					
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MWS		V. Subramanian et al., "A Bulk-Si-Compatible Ultrathin-body SOI Technology for Sub-100nm MOSFETS" Proceeding of the 57th Annual Device Research Conference, pp. 28-29 (1999)			
MWS		Hisamoto et al., "A Folded-channel MOSFET for Deepsub-tenth Micron Era", 1998 IEEE International Electron Device Meeting Technical Digest, pp 1032-1034 (1998)			
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MWS		International Search Report PCT/US 03/26242			
MWS		International Search Report PCT/US 03/39727			
MWS		International Search Report PCT/US 03/40320			
		/Matthew Such/			10/15/2006

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MWS		US-	6,709,982 B1	03/23/2004	Buynoski et al.	
MWS		US-	6,645,797 B1	11/11/2003	Buynoski et al.	
MWS		US-	6,475,869 B1	11/05/2002	Yu	
MWS		US-	5,545,586	08/13/1996	Koh	
MWS		US-	2004/0256647 A1	12/23/2004	Lee et al.	
MWS		US-	2003/0057486 A1	03/27/2003	Gambino et al.	
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		Country Code <sup>3</sup>	Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
MWS		EP	1 202 335 A	05/02/2002	IBM	Page 6, par 22	
MWS		JP	2002298051	10/17/2003	Lee Soko	Abstract, Fig 11	

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MWS		BURENKOV, A. et al., "Corner Effect in Double and Triple Gate FINFETs", European Solid-State Device Research, 2003 33 <sup>rd</sup> Conference on Essderc '03 September 2003, Piscataway, NJ, USA, IEEE, pages 135-138, XP010676716			
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MWS		US-	2004/0227187	11-18-2004	Cheng et al.	Fig. 9B-10C
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MWS		EP	1 566 844 A2	08-24-2005	Samsung Electronics Co.		

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MWS		International Search Report PCT/US2005/000947, Int'l. filing date January 10, 2005, mailed May 3, 2005 (7 pages).	
MWS		T. Park et al., "Fabrication of Body-Tied FinFETs (Omega MOSFETS) Using Bulk Si Wafers", 2003 Symposia on VLSI Technology Digest of Technical Papers, June 2003, pages 135-136.	

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Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T <sup>2</sup>
MWS		JING GUO, et al. "Performance Projections for Ballistic Carbon Nanotube Field-Effect Transistors", Applied Physics Letters, Vol. 80, No. 17, pp. 3192-3194 (April 29, 2004)			
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<b>INFORMATION DISCLOSURE</b> <b>STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>			Application Number	10/607,769
			Filing Date	June 27, 2003
			First Named Inventor:	Scott A. Hareland, et al.
			Art Unit	2812
			Examiner Name	Unassigned
Sheet <b>1</b> of <b>3</b>			Attorney Docket No.:	42P15685

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (If known)				
MWS		US-	2003/0122186 A1	07-03-2003	Sekigawa et al.	
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FOREIGN PATENT DOCUMENTS								
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		Country Code <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
MWS		WO	2004/059726 A1		07-15-2004	IBM Corporation		

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MWS		International Search Report PCT/US2005/033439, mailed 1/31/2006 (7 pgs.)	
MWS		International Search Report PCT/US2005/035380, mailed 2/13/2006 (14 pgs.)	
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MWS		FU-LIANG YANG, et al., "5nm-Gate Nanowire FinFET" 2004 Symposium on VLSI Technology Digest of Technical Papers, 2004 IEEE, pgs. 196-197	
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